# High Efficiency DC-AC Inverters for Alternative Energy Systems



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### About Me

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#### **Project Introduction**

Modern power grids allow wide distribution of Alternating Current (AC) Power, but many sources of of renewable power output Direct Current (DC). Power Inversion allows for integration of sustainable energy sources to the power grid, but modern inverters have inefficiencies and low tolerances due to limitations related to the use of silicon substrates. Our inverter design incorporates GaN wide-bandgap semiconductor materials in order to greatly reduce waste and increase flexibility in connecting sustainable energy sources to the power grid.



Figure 1 Assembled Inverter package

### State of Knowledge

Wide-bandgap semiconductors are less studied than traditional silicon based semiconductors, but exhibit more promising results at higher operating operating temperatures and powers, thereby making them more appealing for power electronics applications.



#### **Relation Sustainable Mg.**

Improved inverters provide raw power resources for manufacturing purposes and allow manufacturinglevel forward supply chain sustainability improvements. Increased efficiency leads to lower power usage and cost in manufacturing systems.



### Approach

12.

Incorporating Gallium Nitride (GaN) based MOSFET semiconductors into a single phase full bridge inverter with active and passive filter components allows for the creation of an inverter with MHz level switching frequency and smaller passive components while also reducing the overall inefficiencies associated with switching losses in larger MOSFET devices.

61.4434 57.2856 53.1277

48.9699 44.8121 40.6543 36.4965 32.3386 28.1008

Figure 3

**FEA Model** 

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#### Results

Inverter testing at 0.5kW, 1.5kW, and 2.0kW power levels indicates an overall inverter efficiency of 98%. Thus it is a significant improvement over conventional designs.



Figure 4 Inverter testing at

#### Conclusions

Our inverter design greatly reduces overall system size, complexity, and waste relative to commercial inverters. Integration of GaN based MOSFETs shows great promise for high power applications. Future testing of the design would include testing at 3-8kW power levels.

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#### References

1.Anthon, Alexander, Zhe Zhang, Michael A.e. Andersen, and Toke Franke. "Efficiency Investigations of a 3kW T-Type Inverter fo Switching Frequencies up to 100 KHz." 2014 International Power Electronics Conference (IPEC-Hiroshima 2014 - ECCE ASIA) (2014): n. pag. Web.

2.Pérez-Tomás, A., M. Placidi, N. Baron, S. Chenot, Y. Cordier, J. C. Moreno, A. Constant, P. Godignon, and J. Millán. "GaN Transistor Characteristics at Elevated Temperatures." J. Appl. Phys. Journal of Applied Physics 106.7 (2009): 074519. Web.

